



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Jason Wei

Assignee: Rambus Inc.

Title: "System and Method for Selecting Optimal Data Transition Types for Clock and Data Recovery"

Serial No.: 10/797,443 Filed: March 9, 2004

Examiner: Unknown Tel: Unknown

Docket No.: RA327.P.US Art Unit: Unknown

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §1.56, §1.97 and §1.98, Applicant brings the 5 documents listed on the enclosed form PTO-1449 to the Examiner's attention in the above-captioned application. Citation of the listed documents shall not be construed as:

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3. an admission that the information cited is, or is considered to be, material to patentability as defined in §1.56(b).

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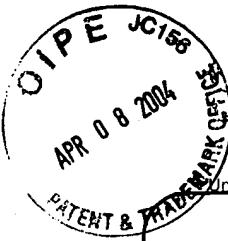
H.5.04

Date

Laurie Moreno

Respectfully submitted,

Arthur J. Behiel  
Attorney for Applicants  
Reg. No. 39,603



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## TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

TRANSMITTAL FORM  (to be used for all correspondence after initial filing)	Application Number	10/797,443	
	Filing Date	03/09/04	
	First Named Inventor	Jason Wei	
	Art Unit	Unknown	
	Examiner Name	Unknown	
Total Number of Pages in This Submission	3	Attorney Docket Number	RA327.P.US

ENCLOSURES (Check all that apply)			
<input type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input type="checkbox"/> Amendment/Reply <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input checked="" type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Response to Missing Parts/Incomplete Application <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation <input type="checkbox"/> Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s) _____	<input type="checkbox"/> After Allowance communication to Technology Center (TC) <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to TC (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Other Enclosure(s) (please identify below): <ul style="list-style-type: none"> <li>1. 1 page PTO-1449 form</li> <li>2. Return Receipt Postcard</li> <li>3. Copy of each cited reference</li> </ul>	
Remarks			

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	Silicon Edge Law Group LLP Arthur J. Behiel, Patent Attorney
Signature	
Date	April 5, 2004

CERTIFICATE OF TRANSMISSION/MAILING		
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Typed or printed name	Laurie Moreno	
Signature		Date 4.5.04

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U.S. Department of Commerce, Patent and Trademark Office		Serial No.: 10/797,443
		Filing Date: March 9, 2004
SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Inventor: Jason Wei
"SYSTEM AND METHOD FOR SELECTING OPTIMAL DATA TRANSITION TYPES FOR CLOCK AND DATA RECOVERY"		Group Art Unit: Unknown
		Examiner Name: Unknown
		Attorney Docket No.: RA327.P.US

## U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	A						
	B						
	C						
	D						

## Foreign Patent Documents

							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
	E							

## OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

F	MARTIN, Aaron et al., "8 Gb/s Differential Simultaneous Bidirectional Link with 4mV 9ps Waveform Capture Diagnostic Capability." September 2003, IEEE International Solid-State Circuits Conference. Session 4/Clock Recovery and Backplane Transceivers/paper 4.5. 10 pages.
G	KASTURIA, Sanjay and WINTERS, Jack H., "Techniques for High-Speed Implementation of Nonlinear Cancellation." June 1991, IEEE Journal on Selected Areas in Communications Vol. 9. No. 5. Pages 711-717.
H	WINTERS, Jack H. and KASTURIA, Sanjay, "Adaptive Nonlinear Cancellation for High-Speed Fiber-Optic Systems." July 1992, IEEE Journal of Lightweight Technology, Vol. 10, No. 7. Pages 971-977.
I	PARHI, Keshab K., "Pipelining in Algorithms with Quantizer Loops." July 1991. IEEE Transactions on Circuits and Systems, Vol. 38, No. 7. Pages 745-754.
J	PARHI, Keshab K., "High-Speed Architectures for Algorithms with Quantizer Loops." August 1990. Proc. ISCAS. Pages 2357-2360.

Examiner	Date Considered
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.